Serial No:

Docket No.: RD-29,472-2

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

RICHARD S. ZHANG, ET AL

Serial No.:

Filed:

Group Art Unit:

For:

CROSS CURRENT CONTROL FOR POWER CONVERTER SYSTEMS AND

INTEGRATED MAGNETIC CHOKE ASSEMBLY

## INFORMATION DISCLOSURE STATEMENT CONTINUING APPLICATION

Honorable Assistant Commissioner of Patents Washington, DC 20231

SIR:

This Information Disclosure Statement is being filed under 37 C.F.R. §1.56 for a continuing application, which relies on the filing date of its parent application, such parent application being identified as:

U.S. Patent Application Serial No:

10/064,547

Examiner:

R. PATEL

Filed:

7/25/02

Inventor(s):

RICHARD S. ZHANG, ET AL

Title:

CROSS CURRENT CONTROL FOR POWER CONVERTER SYSTEMS

AND INTEGRATED MAGNETIC CHOKE ASSEMBLY

Enclosed is a listing of all "prior art" cited by the applicant(s) and submitted in the parent application on PTO-1449 and Form PTO-892 listing "prior art" cited by the Examiner in the parent application. Pursuant to 37 C.F.R. §1.98(d), no actual copies of the documents listed are being furnished to the PTO with this Information Disclosure Statement.

Respectfully submitted,

ANN M. AGOSTI

PATENT COUNSEL

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am. cz to

General Electric Company CRD Patent Docket Room 4A590000 PO Box 8, Bldg. K-1 Schenectady, New York 12301

DATE: 30ec 63

FORM 270-1449 (REV. 7-80)			RTMENT OF COMMERCE ND TRADEMARK OFFICE	ATTY. DOCK RD-29,47		SERIAL NO.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant PICHARD S. ZHANG ET AL		
		<u>LIST OF ITEMS</u>			RICHARD S. ZHANG, ET Filing Date		Group
**		U.S. P	ATENT DO	OCUMENTS CITED BY	APPLICAN	Γ(S)	
*EXAMINER		OUNTENT AN IMPER	DATE	NAME	01400	011001.400	FILING DATE
INITIAL	AA	6,169,677	1/2/01	NAME KITAHATA, ET AL	CLASS	SUBCLASS	IF APPROPRIATE
	AB	5,446,645	8/29/95	SHIRAHAMA, ET AL			
	AC	5,212,630	5/18/93	YAMAMOTO, ET AL			
	AD	5,726,615	3/10/98	BLOOM			
	AE	5,319,343	6/7/94	JEFFRIES			
			NT DOCUN	MENTS CITED BY EXA	MINER ON	PTO 892	
	BA	5,446,645	8/1995	SHIRAHAMA, ET AL			
	BB	5,909,367	6/1999	CHANGE, JIE	<u> </u>		
-	BC BD	6,169,677	1/2001	KITAHATA, ET AL			
	DA DB	Hiroshi Ohshima and Kazuto Kawakami, "Large Capacity 3-Phase UPS with IGBT PWM Inverter", 1991 IEEE, PP. 117-122.  Tako Kawabata, Shigenori Higashino, "Parallel Operation Of Voltage Source Inverters," IEEE Transactions on Industry					
		Applications, Vol. 24, No. 2, March/April 1988, pp. 281 - 287.					
	DC	J.F.Chen, C.L.Chu, C.L. Huang, "The Parallel Operation Of Two UPS By The Coupled-Inductor Method," IEEE, PP. 733-736.					
	DD	Keiju Matsui, Yoshihiro Murai, Makoto Watanabe, Mitsutaka Kaneko, and Fukashi Ueda, "A Pulsewidth-Modulated Inverter with Parallel Connected Transistors Using Current-Sharing Reactor," IEEE Transactions on Power Electronics, Vol. 8, No. 2, April 1993, pp. 186 - 191.					
	DE	Satoshi Ogasawara, Jin Takagaki, Hirofumi Akagi, and Akira Nabae, "A Novel Control Scheme of a Parallel Current-Controlled PWM Inverter," IEEE Transactions on Industry Applications, Vol. 28, No. 5, Sept/October, 1992, pp. 1023-1030.					
	DF	Yoshihiro Komatsuzako, "Cross Current Control for Parallel Operating Three Phase Inverter," 1994 IEEE,pp. 943-950.					
	DG	Kun Xing, Fred C. Lee, Dusan Borojevic, Zhihong Ye, and Sudip Mazumder, "Interleaved PWM with Discontinuous Space-Vector Modulation," IEEE Transactions on Power Electronics, Vol. 14, No. 5, September 1999, pp. 906 - 917.					
EXAMINER	127		****		DATE CON		
		nce considered, whether nunication to applicant	r or not citation is in	conformance with MPEP 609; Draw line thro	ough citation if not in c	onformance and no	t considered. Include copy